Capacitors

INTRODUCTION

The basic function of a $capacitor^1$ is to store charge and thereby electrical energy. This energy can be retrieved at a later time for a variety of uses. Often, multiple capacitors are combined together to form a single circuit. There are two fundamental ways of connecting capacitors together: in series and in parallel. In this lab, you will explore how charge and energy are stored on both series and parallel combinations of capacitors.

DISCUSSION OF PRINCIPLES

A capacitor is a set of two metal conductors separated by a small distance. Usually some type of dielectric (non-conducting) material² is placed between the two conductors. An example is the parallel plate capacitor shown in Fig. 1.

When connected to a voltage source, such as a battery, the two conducting plates become charged. When the battery is first connected, free electrons inside the top capacitor plate will move toward the positive terminal of the battery. This continues until the top capacitor plate is at the same potential as the positive terminal of the battery. A net positive charge, +Q, will then be on the top capacitor plate. At the same time, free electrons inside the wire connecting the negative terminal to the bottom capacitor plate will move toward the bottom capacitor plate. This continues until the bottom capacitor plate is at the same potential as the battery's negative terminal. A net negative charge, -Q, will be present on the bottom capacitor plate.

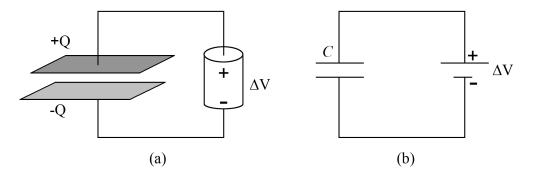


Figure 1: Capacitor circuit

Fig. 1(a) shows a parallel-plate capacitor connected to a battery, and Fig. 1(b) is the schematic diagram of the same setup. Note that the symbols for a capacitor and a battery are similar but not the same. Be careful not to confuse them.

Note that there are equal and opposite charges (+Q and -Q) on the two plates of the charged

¹http://en.wikipedia.org/wiki/Capacitor

²http://en.wikipedia.org/wiki/Capacitor#Dielectric_materials

capacitor. For convenience, we simply refer to the magnitude of the charge and say that the capacitor has a charge Q on it. Also note that since the top capacitor plate is at the same potential as the positive battery terminal and the bottom plate is at the same potential as the negative battery terminal, the potential difference between the capacitor's two plates is the same as the potential difference ΔV across the terminals of the battery. The amount of charge deposited on a capacitor is directly proportional to the potential difference placed across its plates. This can be written as

$$Q = C\Delta V \tag{1}$$

where the proportionality constant C is known as the *capacitance*³ of the capacitor and is measured in units of Farads⁴ (F). The capacitance depends on the particular geometry of the capacitor as well as the type of dielectric material used.

If you were to remove the battery from a charged capacitor, the charges would stay on the plates, and a potential difference would remain between the two plates of the capacitor. Therefore, we say that the capacitor "stores" electrical energy⁵. The amount of energy stored in a charged capacitor can be calculated using the formula

$$E = \frac{1}{2}C(\Delta V)^2.$$
(2)

By using Eq. (1), this formula can be rewritten in the two other equivalent forms

$$E = \frac{1}{2} \cdot \frac{Q^2}{C} \tag{3}$$

or

$$E = \frac{1}{2}Q\Delta V.$$
(4)

Capacitors Connected in Parallel

Frequently, we want to combine multiple capacitors into one circuit. One way to combine capacitors is to connect them in parallel⁶ as shown in Fig. 2. Note that the top plates of both capacitors are connected to the positive terminal of the battery, and the bottom plates of both capacitors are connected to the negative terminal. Therefore, the potential difference across each individual capacitor will be the same as the total potential difference placed by the battery across the whole parallel arrangement.

³http://en.wikipedia.org/wiki/Capacitance

⁴http://en.wikipedia.org/wiki/Farads

⁵http://en.wikipedia.org/wiki/Capacitor#Energy_storage_2

⁶http://en.wikipedia.org/wiki/Capacitors_in_parallel#Cparallel

$$\Delta V_1 = \Delta V_2 = \Delta V \tag{5}$$

Each individual capacitor will store different charges, Q_1 and Q_2 . The total charge placed on the entire parallel arrangement by the battery is then

$$Q_{\text{total}} = Q_1 + Q_2. \tag{6}$$

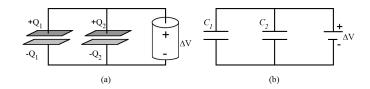


Figure 2: Sketch and schematic of two capacitors in parallel arrangement

The total capacitance of the parallel arrangement, C_{total} , will be different from either of the individual capacitances, C_1 or C_2 . To determine this equivalent capacitance, first note that we can use Eqs.(1) and (5) to write $Q_{\text{total}} = C_{\text{eq}}\Delta V$, $Q_1 = C_1\Delta V$, and $Q_2 = C_2\Delta V$. Substituting these expressions into Eq. (6) and canceling the common factor of ΔV , we get

$$C_{\text{total}} = C_1 + C_2. \tag{7}$$

This is typically called the *equivalent capacitance* and is denoted by C_{eq} . So for a parallel arrangement made up of N different capacitors, $C_{eq} = C_1 + C_2 + C_3 + ... + C_N$. The energy E_{total} stored in the parallel arrangement can be calculated using Eq. (2) as

$$E_{\text{total}} = \frac{1}{2} C_{\text{eq}} (\Delta V)^2$$

= $\frac{1}{2} (C_1 + C_2) (\Delta V)^2$ (8)
= $\frac{1}{2} C_1 (\Delta V)^2 + \frac{1}{2} C_2 (\Delta V)^2$.

Notice that the total energy stored in the parallel arrangement is equal to the sum of the energies stored in each of the individual capacitors.

Capacitors Connected in Series

Another way to combine capacitors is to connect them in series⁷ as shown in Fig. 3. When connected to a battery, the bottom plate of the first capacitor C_1 will store a charge -Q and the top plate of the second capacitor C_2 will store a charge +Q. We see then that the total charge placed on the overall series arrangement by the battery is Q. Notice that the top plate of C_1 and the bottom plate of C_2 are connected by a conducting wire, forming a single H-shaped conductor. This conductor is not connected to the battery, so the battery cannot directly provide charge to

⁷http://en.wikipedia.org/wiki/Capacitors_in_series#Series_circuits

it. Instead, the negative charge -Q on the bottom plate of C_1 and the positive charge +Q on the top plate of C_2 will cause free electrons in the H-shaped conductor to move to the top. The result is an induced net charge of +Q on the top plate of C_1 and an induced net charge of -Q on the bottom plate of C_2 . Therefore, both capacitors will have the same charge Q, which is the same as the total charge on the entire arrangement.

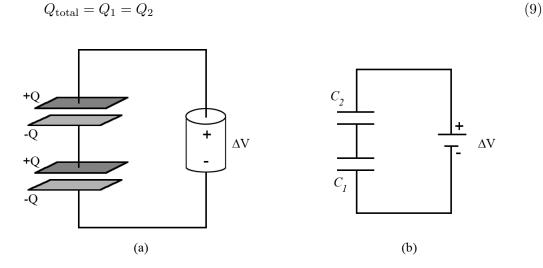


Figure 3: Sketch and schematic of two capacitors in series arrangement

The bottom plate of C_1 is at the same potential as the negative terminal of the battery and the top plate of C_2 is at the same potential as the positive terminal. The H-shaped conductor will have a potential somewhere in between these two values. So, moving from the top plate to the bottom plate of C_2 , the potential will drop by ΔV_2 . Then, moving from the top plate to the bottom plate of C_1 , the potential will drop by ΔV_1 . The potential difference ΔV across the entire series arrangement is equal to the sum of the potential differences across each individual capacitor.

$$\Delta V = \Delta V_1 + \Delta V_2 \tag{10}$$

We can use Eq. (1) to write the potential difference across the individual capacitors and the parallel arrangement as the following.

$$\Delta V = \frac{Q_{\text{total}}}{C_{\text{eq}}} \tag{11}$$

$$\Delta V_1 = \frac{Q_{\text{total}}}{C_1} \tag{12}$$

$$\Delta V_2 = \frac{Q_{\text{total}}}{C_2} \tag{13}$$

Substituting these expressions into Eq. (10), using Eq. (9), and canceling the common factor, Q_{total} , gives

$$\frac{1}{C_{\rm eq}} = \frac{1}{C_1} + \frac{1}{C_2}.$$
(14)

The energy stored in the total series arrangement can be calculated using Eq. (4),

$$E_{\text{total}} = \frac{1}{2}Q_{\text{total}}\Delta V$$

$$= \frac{1}{2}Q_{\text{total}}(\Delta V_1 + \Delta V_2)$$

$$= \frac{1}{2}Q_{\text{total}}\Delta V_1 + \frac{1}{2}Q_{\text{total}}\Delta V_2.$$
 (15)

Note that this is the sum of the energies stored in each individual capacitor.

Listed below are the equations governing voltage distribution, charge distribution, equivalent capacitance, and energy storage for the two types of arrangements.

Parallel Arrangement	Series Arrangement
$\Delta V = \Delta V_1 = \Delta V_2$	$\Delta V = \Delta V_1 + \Delta V_2$
$Q_{\text{total}} = Q_1 + Q_2$	$Q_{\text{total}} = Q_1 = Q_2$
$C_{\rm eq} = C_1 + C_2$	$\frac{1}{C_{\rm eq}} = \frac{1}{C_1} + \frac{1}{C_2}$
$E_{\text{total}} = E_1 + E_2$	$E_{\text{total}} = E_1 + E_2$

OBJECTIVE

The objective of this experiment is to study the characteristics of capacitors in series and parallel arrangements and to qualitatively investigate the energy stored in such arrangements.

EQUIPMENT

Signal interface with power output DataStudio software Multimeter Capacitance meter Two different capacitors One LED Stopwatch

PROCEDURE

Please print the worksheet for this lab. You will need this sheet to record your data.

This lab consists of four parts. The first procedure is a qualitative investigation of the energy stored by different arrangements of capacitors. You will charge both a series arrangement and a parallel arrangement of capacitors and in each case use the stored energy to light up a small LED bulb. You will predict which arrangement stores more energy based on the amount of time the bulb remains lit. In the remaining procedures, you will use a capacitance meter and a voltmeter to make quantitative measurements on parallel and series arrangements of capacitors. You will compare your data with the theoretical equations for voltage distribution, charge distribution, equivalent capacitance, and energy storage.

<u>CAUTION</u>: Before connecting the power supply to the circuit, ask your TA to check your circuit connections to make certain that your capacitors are connected correctly.

Procedure A: Lighting an LED

- **1** Open the appropriate DataStudio file associated with this lab. The screen should contain a signal generator window similar to the one shown in Fig. 4.
- 2 In the signal generator window select DC.In the DC voltage field, type 5.0.Click **ON**.

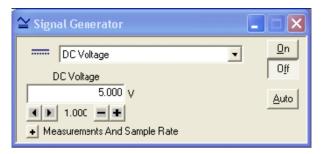


Figure 4: Signal generator window

Bischarge both of your capacitors by shorting them out for at least 10 seconds as shown in Fig. 5.

You can check to see if the capacitors are fully discharged by using the voltmeter to read the potential difference across each capacitor. See Appendix K.

The readings should be very close to 0.0 volts.



Figure 5: Discharging a capacitor

4 Connect the two capacitors in series. They must be connected so that the arrows printed on each capacitor are facing the same direction as shown in Fig. 6.

The red output lead must be connected to the notched end of one capacitor (this is also the end with the black face) while the black lead must be connected to the end that has the arrows pointing toward it (this is the end with the silver face). This will charge the capacitors.



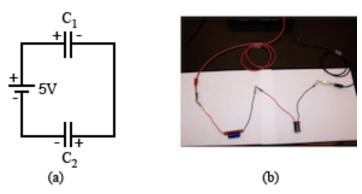


Figure 6: Circuit diagram and photo of capacitors in series

- 5 Disconnect the power output from the circuit by removing the red and black leads from the capacitors. **DO NOT** turn off the power output or set its voltage to zero.
- 6 Connect the LED to the capacitor arrangement as shown in Fig. 7.The red and black LED leads should be connected the same way the red and black power amplifier leads were connected.Immediately begin timing how long the LED remains lit. Record the time on the worksheet.

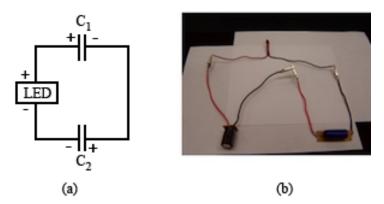


Figure 7: Circuit diagram and photo for LED connection

- 7 Disconnect your series arrangement, and fully discharge both capacitors.
- 8 Connect the two capacitors in parallel. Again, the arrows should point in the same direction as shown in Fig. 8.

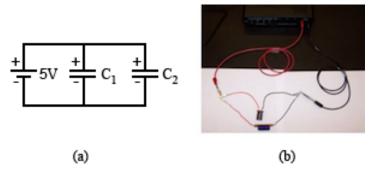


Figure 8: Circuit diagram and photo of capacitors in parallel

- **9** Repeat steps 3 through 6 for the parallel arrangement referring to Fig. 8. Record the time for the parallel arrangement on the worksheet.
- $10\,$ Now use just the 1000- μF capacitor and see how long the LED remains lit. Record this time on the worksheet.

<u>CHECKPOINT 1</u>: Ask your TA to check your work before proceeding.

Procedure B: Capacitors in Parallel

11 Set the capacitance meter to the appropriate setting.

Measure the capacitance of each individual capacitor. Record the capacitances in Data Table 1.

When measuring capacitance, the uncertainty is $\pm 5\%$ of the displayed value.

- 12 Connect the two capacitors in parallel.
- 13 Using the capacitance meter, measure the total (or equivalent) capacitance of the parallel arrangement. Record this value in Data Table 1.
- 14 Leave the parallel arrangement connected and discharge the capacitors by shorting them out one at a time.
- 15 Use the multimeter to make sure they are fully discharged. See Appendix K.
- 16 Connect the parallel arrangement to the power output leads as shown in Fig. 8.
- 17 Turn the multimeter on and switch to the proper DC voltage scale. For this multimeter, the uncertainty in the potential difference is $\pm 1\%$ of the displayed value.
- 18 For this step, it is important that you **do not touch** the capacitor leads when you are measuring the voltage. You are a resistor, just like the LED, so you are capable of discharging the capacitor by touching it.

Measure and record the potential difference ΔV_1 and ΔV_2 across C_1 and C_2 , respectively. Record these values in Data Table 1.

- 19 Measure the potential difference $\Delta V_{\rm p}$ across the parallel arrangement, and record this in Data Table 1.
- **20** Using the measured values of capacitance and voltage and Eq. (1), calculate the charges and associated uncertainties for the individual capacitors and the parallel arrangement. Record these values in Data Table 1.
- **21** Using Eqs. (2) and (8) and the measured values of the capacitances, calculate the energy stored in each individual capacitor and the associated uncertainties for the parallel arrangement. Record these values in Data Table 1.
- **22** Using Eq. (7), calculate the theoretical equivalent capacitance and the theoretical uncertainty for the two capacitors in parallel. Record this value in the worksheet.
- 23 Calculate the percent difference between the theoretical value and the measured value (see Data Table 1) of the equivalent capacitance. See Appendix B.
- **24** Using Eq. (6), calculate the theoretical total charge and its associated uncertainty for the two capacitors in parallel. Record this value on the worksheet.
- 25 Calculate the percent difference, between the theoretical value and the measured value of the total charge, and record this on the worksheet.

<u>CHECKPOINT 2</u>: Ask your TA to check your table and calculations.

Procedure C: Capacitors in Series

- 26 Record the previously measured capacitances of the two capacitors in Data Table 2.
- 27 Connect the two capacitors in series.
- **28** Using the capacitance meter, measure the total capacitance of the series arrangement. Record this value in Data Table 2.
- **29** Leave the series arrangement connected and discharge the capacitors by shorting them out one at a time. Make sure they are fully discharged.
- **30** Connect the power amplifier to the arrangement as shown in Fig. 6.
- 31 Measure the voltage across each individual capacitor. Record your measurements in Data Table2. Again, be careful not to touch the leads when recording the voltages.
- **32** Measure the voltage $\Delta V_{\rm s}$ across the entire series arrangement. Record this value in Data Table 2.
- **33** Using the measured values of capacitance and voltage, calculate the charge stored on each capacitor and the associated uncertainties. Record these values in Data Table 2.
- **34** Using measured values for the total capacitance and voltage and Eq. (1), calculate the charge in the series arrangement, and record these values in Data Table 2.
- **35** Using Eqs. (2) and (15) and the measured values of the capacitances, calculate the energies stored in the individual capacitors and the series arrangement and the associated uncertainties. Record these values in Data Table 2. Use the formula given below to calculate the uncertainty in the equivalent capacitance.

$$\sigma_{C_{\text{eq}}} = C_{\text{eq}}^2 \sqrt{\left(\frac{\sigma_{C_1}}{C_1^2}\right)^2 + \left(\frac{\sigma_{C_2}}{C_2^2}\right)^2} \tag{16}$$

- **36** Calculate the theoretical capacitance and its uncertainty for the series arrangement using Eq. (14). Record this value in the worksheet.
- **37** Calculate the percent difference between the theoretical value and the measured value (from Data Table 2) of the equivalent capacitance, and record this on the worksheet.
- **38** Using Eq. (10), calculate the theoretical voltage and its uncertainty for the series arrangement. Record this value on the worksheet.
- **39** Calculate the percent difference between this theoretical value and the measured value (from Data Table 2) of the total voltage and record this on the worksheet.

<u>CHECKPOINT 3</u>: Ask your TA to check your table and calculations.